

PATENT

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APR 20 2004

Applicant(s): James John Casto et al.

Title: INTEGRATED CIRCUIT PACKAGE INCORPORATING
PROGRAMMABLE ELEMENTS

Application No.: 09/484,311

Filed: January 18, 2000

Examiner: Eugene Lee

Group Art Unit: 2815

Atty. Docket No.: 1001-0087

Confirmation No.: 9539

OFFICIAL

April 20, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**APPLICANT INITIATED INTERVIEW SUMMARY (37 C.F.R. § 1.133(b))**

A telephonic interview was conducted on the above-identified application on April 20, 2004. The undersigned discussed arguments presented in the Response to Non-Final Office Action, filed April 14, 2004, with Examiner Lee and Examiner Thomas. No agreement was reached. In particular, the undersigned and the Examiners discussed limitations of claim 2. The package and die are two separate structures and therefore, the package of Andoh (U.S. Patent No. 6,674,163) in combination with the integrated circuit including a fuse of MacPherson (U.S. Patent No. 6,369,437) fails to teach a package for mounting at least one integrated circuit die, the package comprising at least one one-time programmable element, as claimed by the Applicant.

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Respectfully submitted,

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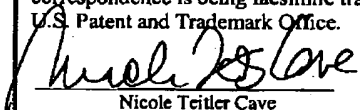
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ATTACHED HERETO:

(1) APPLICANT INITIATED INTERVIEW SUMMARY (37 C.F.R. § 1.133(b)) – 2 pages

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